

ABSTRACT OF THE DISCLOSURE:

There is disclosed a method for evaluating measured data, in which the measured data are digitalized and the digitalized measured data are disseminated to a processor, processed in the processor and the results are output. The digitalized measured data are intermediately stored in a shift register, in particular a FIFO memory, until completion of the acquisition of all measured data to be processed simultaneously, and read out together. After having read out all memory values necessary for processing, the computation of the results is executed in the processor. A device for obtaining measured values from measured data includes an A/D converter, a digital signal processor and a shift register arranged between the A/D converter and the digital signal processor.

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